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**1 An 800 MHz Adjustment-Free Multistage MIC Power Amplifier**
*Akiyama, M.; Kawakami, Y.; Kaminishi, K.;*

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*Yamada, T.; Hatano, K.; Morimoto, M.; Furumiya, M.; Nakashiba, Y.; Uchiya, Tanabe, A.; Kawakami, Y.; Nakano, T.; Kawai, S.; Suwazono, S.; Utsumi, H.; Katoh, S.; Syohji, D.; Taniji, Y.; Mutoh, N.; Orihara, K.; Teranishi, N.; Hokari* Electron Devices, IEEE Transactions on , Volume: 48 , Issue: 2 , Feb. 2001

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*Tanabe, A.; Kudoh, Y.; Kawakami, Y.; Masubuchi, I.; Kawai, S.; Yamada, T.; Morimoto, M.; Arai, K.; Hatano, K.; Furumiya, M.; Naliashiba, Y.; Mutoh, N.; Orihara, K.; Teranishi, N.;*

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*Imaizumi, Y.; Suzuki, Y.; Kawakami, Y.; Araki, K.;*

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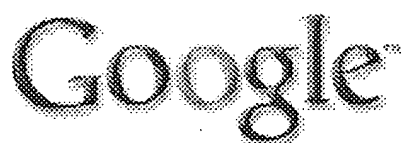
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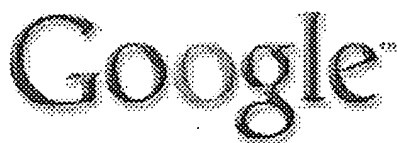


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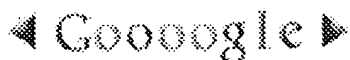
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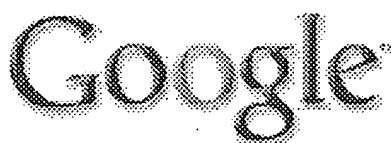


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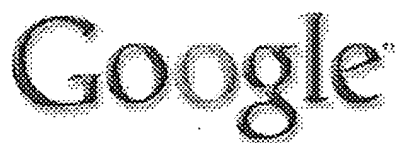
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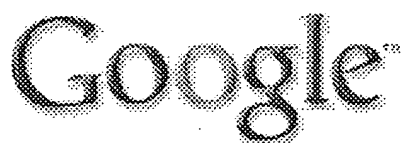


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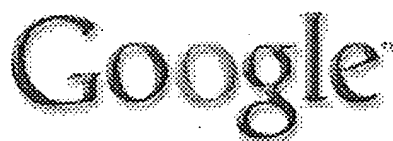
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*Katayama, A.; Sakagawa, Y.; Tamura, H.;*

Image Processing, 1999. ICIP 99. Proceedings. 1999 International Conference ,Volume: 3 , 24-28 Oct. 1999

Pages:26 - 30 vol.3

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**2 Packaging technologies for 500-MHz VLSI test system ULTIMATE**
*Sakagawa, Y.; Akazawa, Y.; Narumi, N.; Yoshii, A.; Sudo, T.;*

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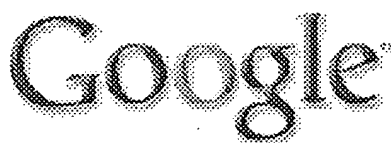
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... on DNA, **Yuichi Kawakami**, Makoto Chikira, Yuki Fujii, 10th SPACC, Index. P, 15, The Effect of Acridine Moiety for Reaction of Cu(II)[1, 4, 7-triazacyclononane], 34. ...

[www.ns.kogakuin.ac.jp/~wwf1014/SPACC10/IndexPage.htm](http://www.ns.kogakuin.ac.jp/~wwf1014/SPACC10/IndexPage.htm) - 68k - [Cached](#) - [Similar pages](#)**[PDF] Rapport de certification 2001/11**File Format: PDF/Adobe Acrobat - [View as HTML](#)

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*Hirai, M.; Mochida, T.; Hashimoto, T.; Fujii, E.; Kiyohara, T.;*

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 Pages:939 - 943

[\[Abstract\]](#)   [\[PDF Full-Text \(460KB\)\]](#)   **IEEE JNL**
**2 A stacked-CMOS cell technology for high-density SRAM's**
*Uemoto, Y.; Fujii, E.; Nakamura, A.; Senda, K.; Takagi, H.;*

 Electron Devices, IEEE Transactions on , Volume: 39 , Issue: 10 , Oct. 1992  
 Pages:2359 - 2363

[\[Abstract\]](#)   [\[PDF Full-Text \(512KB\)\]](#)   **IEEE JNL**
**3 A high-voltage polysilicon TFT with multigate structures**
*Uemoto, Y.; Fujii, E.; Emoto, F.; Nakamura, A.; Senda, K.;*

 Electron Devices, IEEE Transactions on , Volume: 38 , Issue: 1 , Jan. 1991  
 Pages:95 - 100

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**4 A laser-recrystallization technique for silicon-TFT integrated circuits on quartz substrates and its application to small-size monolithic active-n LCD's**
*Fujii, E.; Senda, K.; Emoto, F.; Yamamoto, A.; Nakamura, A.; Uemoto, Y.; Kato, G.;*

 Electron Devices, IEEE Transactions on , Volume: 37 , Issue: 1 , Jan. 1990  
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**5 Solid phase growth technique for high cut-off frequency polysilicon integrated circuits on a quartz substrate**
*Emoto, F.; Senda, K.; Fujii, E.; Nakamura, A.; Yamamoto, A.; Uemoto, Y.; Kato, G.;*

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**6 A CPD image sensor with an SOI structure**

*Fujii, E.; Senda, K.; Emoto, F.; Horoshima, Y.;*

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**7 0.18/ $\mu$ m SBT-based embedded feram operating at a low voltage of 1.1V**

*Nagano, Y.; Mikawa, T.; Kutsunai, T.; Hayashi, S.; Nasu, T.; Natsume, S.; Tatsunari, T.; Ito, T.; Goto, S.; Yano, H.; Noma, A.; Nagahashi, K.; Miki, T.; Sakagami, M.; Izutsu, Y.; Nakakuma, T.; Hirano, H.; Iwanari, S.; Murakuki, Y. Yamaoka, K.; Goho, Y.; Judai, Y.; Fujii, E.; Sato, K.;*

VLSI Technology, 2003. Digest of Technical Papers. 2003 Symposium on , Jun 12, 2003

Pages:171 - 172

[\[Abstract\]](#) [\[PDF Full-Text \(271KB\)\]](#) IEEE CNF

**8 Physical characteristics and format of a two-inch-diameter magnetic optical disk system with 3-GB capacity**

*Hino, Y.; Birukawa, M.; Ishibashi, K.; Ueda, E.; Matsumoto, T.; Koyama, O.; Nishikawa, K.; Hozumi, Y.; Ashinuma, T.; Fujii, E.;*

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**9 The future of ferroelectric memories**

*Paz de Araujo, C.; McMillan, L.; Joshi, V.; Solayappan, N.; Lim, M.; Arita, K.; Moriwaki, N.; Hirano, H.; Baba, T.; Shimada, Y.; Sumi, T.; Fujii, E.; Otsuki, T.* Solid-State Circuits Conference, 2000. Digest of Technical Papers. ISSCC. 2000 IEEE International , 7-9 Feb. 2000

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**10 Far-infrared magneto-optical absorption of electronic system in quantum dot array**

*Yoshizawa, T.; Fujii, E.; Ohyama, T.; Oto, K.; Takaoka, S.; Murase, K.; Gamc* Microwave and Millimeter Wave Technology Proceedings, 1998. ICMMT '98. 1<sup>st</sup> International Conference on , 18-20 Aug. 1998

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**11 Software Control Of I/O Subsystem On Media Core Processor**

*Hirai, M.; Mochida, T.; Hashimoto, T.; Fujii, E.; Kiyohara, T.;*

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**12 Highly-reliable ferroelectric memory technology with bismuth-layer structured thin films (Y-1 family)**

*Fujii, E.; Otsuki, T.; Judai, Y.; Shimada, Y.; Azuma, M.; Uemoto, Y.; Nagano, Nasu, T.; Izutsu, Y.; Matsuda, A.; Nakao, K.; Tanaka, K.; Hirano, K.; Ito, T.; Mikawa, T.; Kutsunai, T.; McMillan, L.D.; Paz de Araujo, C.A.;*

Electron Devices Meeting, 1997. Technical Digest., International , 7-10 Dec. 1997  
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**13 Ferroelectric nonvolatile memory technology with bismuth layer-structured ferroelectric materials**

*Arita, K.; Shimada, Y.; Uemoto, Y.; Hayashi, S.; Azuma, M.; Judai, Y.; Sumi, Fujii, E.; Otsuki, T.; McMillan, L.D.; De Araujo, C.A.P.;*

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Pages:13 - 16 vol.1

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**14 A 256 kb nonvolatile ferroelectric memory at 3 V and 100 ns**

*Sumi, T.; Moriwaki, N.; Nakane, G.; Nakakuma, T.; Judai, Y.; Uemoto, Y.; Nakano, Y.; Hayashi, S.; Azuma, M.; Fujii, E.; Katsu, S.-I.; Otsuki, T.; McMillan, L.; Paz de Araujo, C.; Kano, G.;*

Solid-State Circuits Conference, 1994. Digest of Technical Papers. 41st ISSCC 1994 IEEE International , 16-18 Feb. 1994  
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**15 ULSI DRAM technology with Ba<sub>0.7</sub>Sr<sub>0.3</sub>TiO<sub>3</sub> film of 1.3nm equivalent thickness and 10<sup>-9</sup> A/cm<sup>2</sup> leakage current**

*Fujii, E.; Uemoto, Y.; Hayashi, S.; Nasu, T.; Shimada, Y.; Matsuda, A.; Kibe, Azuma, M.; Otsuki, T.; Kano, G.; Scott, M.; McMillan, L.D.; Paz de Araujo, C.;*

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**16 Microstructure-induced Schottky barrier effects in barium strontium titanate (BST) thin films for 16 and 64 Mbit (DRAM cells)**
*Scott, J.F.; Azuma, M.; Fujii, E.; Otsuki, T.; Kano, G.; Scott, M.C.; Paz de Ara C.A.; McMillan, L.D.; Roberts, T.;*

Applications of Ferroelectrics, 1992. ISAF '92., Proceedings of the Eighth IEEE International Symposium on , 30 Aug.-2 Sept. 1992

Pages:356 - 359

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**17 1.5V Supply Voltage, Low Standby Current SRAMs**
*Uemoto, Y.; Fujii, E.; Furuta, T.; Shimazaki, T.; Okuyama, H.; Aono, E.; Yoshida, S.; Senda, K.;*

VLSI Technology, 1991. Digest of Technical Papers., 1991 Symposium on , Mar 30, 1991

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**18 A high-reliability, low-operation-voltage monolithic active-matrix LCD by using advanced solid-phase-growth technique**
*Nakamura, A.; Emoto, F.; Fujii, E.; Yamamoto, A.; Uemoto, Y.; Hayashi, H.; Y.;*

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**19 A high-performance stacked-CMOS SRAM cell by solid phase growth technique**
*Uemoto, Y.; Fujii, E.; Nakamura, A.; Senda, K.;*

VLSI Technology, 1990. Digest of Technical Papers. 1990 Symposium on , 4-7 1990

Pages:21 - 22

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**20 A 2/3 inch fully integrated active-matrix LCD with thin mesh polys electrodes**

*Yamamoto, A.; Emoto, F.; Fujii, E.; Nakamura, A.; Uemoto, Y.; Kobayashi, K. Senda, K.;*

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**21 A very small active-matrix LCD panel with monolithically integrated peripheral driver circuits**

*Emoto, F.; Senda, K.; Fujii, E.; Nakamura, A.; Yamamoto, A.; Uemoto, Y.; Kamimura, T.; Kano, G.;*

Electron Devices Meeting, 1988. Technical Digest., International , 11-14 Dec. 1988. Pages:878 - 880

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<u>10151184</u>	Not Issued	030	05/20/2002	IMAGE PICKUP SYSTEM	FUJII, EIRO
<u>10118054</u>	6480288	150	04/09/2002	MEASURING SYSTEM WITH IMPROVED METHOD OF READING IMAGE DATA OF AN OBJECT	FUJII, EIRO
<u>10091989</u>	6466892	150	03/05/2002	METHOD OF COMPOSING THREE-DIMENSIONAL MULTI-VIEWPOINTS DATA	FUJII, EIRO
<u>10075230</u>	Not Issued	061	02/15/2002	MEASURING SYSTEM WITH IMPROVED METHOD OF READING IMAGE DATA OF AN OBJECT	FUJII, EIRO
<u>09985546</u>	Not Issued	041	11/05/2001	IMAGE DISPLAYING APPARATUS	FUJII, EIRO
<u>09893897</u>	Not Issued	030	06/29/2001	METHOD OF AND DEVICE FOR MODIFYING STANDARD MODEL	FUJII, EIRO
<u>09816405</u>	Not Issued	041	03/26/2001	METHOD AND DEVICE FOR FITTING SURFACE TO POINT GROUP, MODELING DEVICE, AND COMPUTER PROGRAM	FUJII, EIRO
<u>09749624</u>	Not	071	12/28/2000	METHOD AND SYSTEM FOR	FUJII, EIRO

	Issued			PROCESSING A THREE-DIMENSIONAL SHAPE DATA	
<u>09748142</u>	Not Issued	061	12/27/2000	METHOD FOR GENERATING SURFACE DATA TO BE ADDED TO THREE DIMENSIONAL SHAPE DATA	FUJII, EIRO
<u>09676767</u>	<u>6529280</u>	150	10/02/2000	THREE-DIMENSIONAL MEASURING DEVICE AND THREE-DIMENSIONAL MEASURING METHOD	FUJII, EIRO
<u>09536686</u>	Not Issued	071	03/28/2000	THREE-DIMENSIONAL DATA INPUT METHOD AND APPARATUS	FUJII, EIRO
<u>09407771</u>	<u>6405151</u>	150	09/28/1999	METHOD OF COMPOSING THREE-DIMENSIONAL MULTI-VIEWPOINTS DATA	FUJII, EIRO
<u>09243794</u>	Not Issued	135	02/03/1999	THREE DIMENSIONAL INFORMATION MEASUREMENT METHOD AND APPARATUS	FUJII, EIRO
<u>08917910</u>	<u>6128405</u>	150	08/27/1997	SYSTEM FOR PROCESSING THREE-DIMENSIONAL SHAPE DATA	FUJII, EIRO
<u>08748935</u>	Not Issued	061	11/13/1996	METHOD FOR GENERATING THREE-DIMENSIONAL FORM DATA AND APPARATUS THEREFOR	FUJII, EIRO
<u>08748325</u>	<u>6141105</u>	150	11/13/1996	THREE-DIMENSIONAL MEASURING DEVICE AND THREE-DIMENSIONAL MEASURING METHOD	FUJII, EIRO
<u>08747819</u>	<u>6346949</u>	150	11/13/1996	THREE-DIMENSIONAL FORM DATA PROCESSOR RETAINING INFORMATION ON COLOR BOUNDARIES OF AN OBJECT WHEN THINNING COORDINATE DATA	FUJII, EIRO
<u>08665623</u>	<u>5990895</u>	150	06/18/1996	THREE-DIMENSIONAL TO TWO DIMENSIONAL DATA PROCESSING AND CONVERSION DEVICE AND METHOD OF USING THE SAME	FUJII, EIRO

Inventor Search Completed: No Records to Display.

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First Name

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**Inventor Name Search Result**

Your Search was:

Last Name = SAKAGAWA

First Name = YOSHIKO

Application#	Patent#	Status	Date Filed	Title	Inventor Name 6
<u>09676767</u>	<u>6529280</u>	150	10/02/2000	THREE-DIMENSIONAL MEASURING DEVICE AND THREE-DIMENSIONAL MEASURING METHOD	SAKAGAWA, YOSHIKO
<u>09536686</u>	Not Issued	071	03/28/2000	THREE-DIMENSIONAL DATA INPUT METHOD AND APPARATUS	SAKAGAWA, YOSHIKO
<u>08748935</u>	Not Issued	061	11/13/1996	METHOD FOR GENERATING THREE-DIMENSIONAL FORM DATA AND APPARATUS THEREFOR	SAKAGAWA, YOSHIKO
<u>08748325</u>	<u>6141105</u>	150	11/13/1996	THREE-DIMENSIONAL MEASURING DEVICE AND THREE-DIMENSIONAL MEASURING METHOD	SAKAGAWA, YOSHIKO
<u>08747819</u>	<u>6346949</u>	150	11/13/1996	THREE-DIMENSIONAL FORM DATA PROCESSOR RETAINING INFORMATION ON COLOR BOUNDARIES OF AN OBJECT WHEN THINNING COORDINATE DATA	SAKAGAWA, YOSHIKO
<u>08665623</u>	<u>5990895</u>	150	06/18/1996	THREE-DIMENSIONAL TO TWO DIMENSIONAL DATA PROCESSING AND CONVERSION DEVICE AND METHOD OF USING THE SAME	SAKAGAWA, YOSHIKO

Inventor Search Completed: No Records to Display.

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Time: 10:15:20

## Inventor Name Search Result

Your Search was:

Last Name = SHIONO

First Name = KOICHI

Application#	Patent#	Status	Date Filed	Title	Inventor Name 13
<u>10627138</u>	Not Issued	020	07/25/2003	SOLID STATE IMAGE SENSOR WITH FIXED PATTERN NOISE REDUCTION	SHIONO, KOICHI
<u>10091989</u>	<u>6466892</u>	150	03/05/2002	METHOD OF COMPOSING THREE-DIMENSIONAL MULTI-VIEWPOINTS DATA	SHIONO, KOICHI
<u>09834431</u>	Not Issued	030	04/13/2001	SOLID-STATE IMAGE PICKUP APPARATUS, ITS DRIVING METHOD, AND CAMERA SYSTEM	SHIONO, KOICHI
<u>09830515</u>	Not Issued	030	08/03/2001	SOLID-STATE IMAGE PICKUP DEVICE, METHOD OF DRIVING THE SAME AND CAMERA SYSTEM	SHIONO, KOICHI
<u>09612654</u>	Not Issued	071	07/07/2000	SOLID-STATE IMAGE PICKUP DEVICE AND METHOD FOR DRIVING THE SAME	SHIONO, KOICHI
<u>09546168</u>	<u>6339215</u>	150	04/11/2000	SOLID STATE IMAGE SENSING DEVICE AND IMAGE SIGNAL PROCESSING METHOD OF THE SAME	SHIONO, KOICHI
<u>09536686</u>	Not Issued	071	03/28/2000	THREE-DIMENSIONAL DATA INPUT METHOD AND APPARATUS	SHIONO, KOICHI
<u>09414180</u>	<u>6556194</u>	150	10/07/1999	METHOD OF MERGING THREE-DIMENSIONAL SHAPE DATA AND METHOD OF REPRESENTING THREE-DIMENSIONAL SHAPE DATA	SHIONO , KOICHI
<u>09407771</u>	<u>6405151</u>	150	09/28/1999	METHOD OF COMPOSING THREE-DIMENSIONAL MULTI-VIEWPOINTS DATA	SHIONO , KOICHI

<u>09327523</u>	Not Issued	071	06/08/1999	SOLID-STATE IMAGING ELEMENT, METHOD FOR DRIVING THE SAME, AND CAMERA SYSTEM	SHIONO , KOICHI
<u>09252690</u>	Not Issued	061	02/22/1999	SOLID STATE IMAGE PICKUP DEVICE, DRIVING METHOD THEREOF AND CAMERA	SHIONO , KOICHI
<u>09134153</u>	<u>6677993</u>	150	08/14/1998	SOLID STATE IMAGE SENSOR WITH FIXED PATTERN NOISE REDUCTION	SHIONO , KOICHI
<u>09106495</u>	<u>6201233</u>	150	06/29/1998	POSITION DETECTION ELEMENT, POSITION DETECTION DEVICE, AND POSITION INPUT DEVICE	SHIONO , KOICHI

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## Inventor Name Search Result

Your Search was:

Last Name = KAWAKAMI

First Name = YUICHI

Application#	Patent#	Status	Date Filed	Title	Inventor Name 30
<a href="#">10607879</a>	Not Issued	020	06/27/2003	IMAGE SERVICE SYSTEM	KAWAKAMI, YUICHI
<a href="#">10601965</a>	Not Issued	020	06/23/2003	IMAGE PROCESSING APPARATUS CAPABLE OF HIGHLY PRECISE EDGE EXTRACTION	KAWAKAMI, YUICHI
<a href="#">10452426</a>	Not Issued	030	06/02/2003	METHOD FOR DETECTING OBJECT FORMED OF REGIONS FROM IMAGE	KAWAKAMI, YUICHI
<a href="#">10375616</a>	Not Issued	020	02/27/2003	MONITORING APPARATUS, MONITORING METHOD, MONITORING PROGRAM AND MONITORING PROGRAM RECORDED RECORDING MEDIUM READABLE BY COMPUTER	KAWAKAMI, YUICHI
<a href="#">09904528</a>	Not Issued	030	07/16/2001	SYSTEM AND METHOD CAPABLE OF APPROPRIATELY MANAGING CUSTOMER INFORMATION AND COMPUTER-READABLE RECORDING MEDIUM HAVING CUSTOMER INFORMATION MANAGEMENT PROGRAM RECORDED THEREIN	KAWAKAMI, YUICHI
<a href="#">09798968</a>	Not Issued	083	03/06/2001	METHOD AND APPARATUS FOR DATA PROCESSING RECOGNIZING AN OBJECT REPRESENTED AS TWO-DIMENSIONAL IMAGE	KAWAKAMI, YUICHI
<a href="#">09536686</a>	Not Issued	071	03/28/2000	THREE-DIMENSIONAL DATA INPUT METHOD AND APPARATUS	KAWAKAMI, YUICHI

<u>09480492</u>	Not Issued	060	01/11/2000	ACCESS CONTROL APPARATUS CAPABLE OF GRANTING FUNCTION OF CONTROLLING SERVER TO AT LEAST ONE CLIENT	KAWAKAMI, YUICHI
<u>09031041</u>	<u>6038501</u>	150	02/26/1998	AUTONOMOUS VEHICLE CAPABLE OF TRAVELING/STOPPING IN PARALLEL TO WALL AND CONTROLLING METHOD THEREOF	KAWAKAMI , YUICHI
<u>08937732</u>	<u>5903124</u>	150	09/25/1997	APPARATUS FOR POSITIONING MOVING BODY ALLOWING PRECISE POSITIONING OF MOVING BODY	KAWAKAMI , YUICHI
<u>08892848</u>	<u>6142252</u>	150	07/11/1997	AUTONOMOUS VEHICLE THAT RUNS WHILE RECOGNIZING WORK AREA CONFIGURATION, AND METHOD OF SELECTING ROUTE	KAWAKAMI , YUICHI
<u>08869978</u>	Not Issued	164	06/05/1997	AUTONOMOUSLY MOVING ROBOT MOVING REFERRING TO A REFERENCE OBJECT	KAWAKAMI , YUICHI
<u>08827132</u>	Not Issued	161	03/27/1997	MOBILE BODY CONTROLLER FOR CONTROLLING A MOBILE BODY BASED ON AN ENVIRONMENT AROUND THE SAME	KAWAKAMI , YUICHI
<u>08826327</u>	Not Issued	168	03/27/1997	OPERATION INSTRUCTION EDITING APPARATUS CAPABLE OF EFFICIENTLY EDITING AN OPERATION INSTRUCTION INCLUDING A SERIES OF PROCEDURES	KAWAKAMI , YUICHI
<u>08789127</u>	<u>6574536</u>	150	01/27/1997	MOVING APPARATUS FOR EFFICIENTLY MOVING ON FLOOR WITH OBSTACLE	KAWAKAMI , YUICHI
<u>08631014</u>	<u>5947225</u>	150	04/12/1996	AUTOMATIC VEHICLE	KAWAKAMI , YUICHI
<u>08518819</u>	<u>5652489</u>	150	08/24/1995	MOBILE ROBOT CONTROL SYSTEM	KAWAKAMI , YUICHI
<u>08496519</u>	<u>5696675</u>	150	06/29/1995	ROUTE MAKING SYSTEM FOR A MOBILE ROBOT	KAWAKAMI , YUICHI
<u>08452280</u>	<u>5720077</u>	150	05/26/1995	RUNNING ROBOT CARRYING	KAWAKAMI ,

				OUT PRESCRIBED WORK USING WORKING MEMBER AND METHOD OF WORKING USING THE SAME	YUICHI
<u>08420652</u>	<u>5613261</u>	150	04/12/1995	CLEANER	KAWAKAMI , YUICHI
<u>07455628</u>	<u>5049791</u>	250	12/22/1989	DEVICE FOR MEASURING A CHARACTERISTIC OF A COLOR CATHODE RAY TUBE	KAWAKAMI , YUICHI
<u>07455627</u>	<u>5032769</u>	150	12/22/1989	A DEVICE FOR MEASURING A CHARACTERISTIC OF A COLOR CATHODE RAY TUBE BASED ON SYNCHRONIZED SIGNALS	KAWAKAMI , YUICHI
<u>07258178</u>	<u>5028849</u>	150	10/14/1988	DEVICE FOR MEASURING CONVERGENCE OF COLOR CATHODE RAY TUBE	KAWAKAMI , YUICHI
<u>07008684</u>	<u>4899301</u>	150	01/29/1987	SIGNAL PROCESSOR FOR RAPIDLY CALCULATING A PREDETERMINED CALCULATION A PLURALITY OF TIMES TO TYPICALLY CARRYING OUT FFT OR INVERSE FFT	KAWAKAMI , YUICHI
<u>06864268</u>	<u>4811268</u>	150	05/19/1986	PROCESSING CIRCUIT CAPABLE OF RAISING THROUGHPUT OF ACCUMULATION	KAWAKAMI , YUICHI
<u>06856668</u>	<u>4742236</u>	150	04/25/1986	FLAME DETECTOR FOR DETECTING PHASE DIFFERENCE IN TWO DIFFERENT WAVELENGTHS OF LIGHT	KAWAKAMI , YUICHI
<u>06727020</u>	<u>4722068</u>	150	04/25/1985	DOUBLE PRECISION MULTIPLIER	KAWAKAMI , YUICHI
<u>06647507</u>	<u>4700324</u>	150	09/05/1984	DIGITAL CIRCUIT PERFORMING AN ARITHMETIC OPERATION WITH AN OVERFLOW	KAWAKAMI , YUICHI
<u>06209250</u>	<u>4379338</u>	150	11/21/1980	ARITHMETIC CIRCUIT WITH OVERFLOW DETECTION CAPABILITY	KAWAKAMI , YUICHI
<u>05934982</u>	<u>4277836</u>	150	08/18/1978	COMPOSITE RANDOM ACCESS MEMORY PROVIDING DIRECT AND	KAWAKAMI , YUICHI

				AUXILIARY MEMORY ACCESS	
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